

Electronic Acknowledgement Receipt

EFS ID:	7000185
Application Number:	09748098
International Application Number:	
Confirmation Number:	4360
Title of Invention:	Instruction level multithreading in an embedded processor using zero-time context switching
First Named Inventor/Applicant Name:	Nicholas J. Kelsey
Customer Number:	00758
Filer:	Brian Gregory Brannon
Filer Authorized By:	
Attorney Docket Number:	20880-05093
Receipt Date:	12-FEB-2010
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Application Type:	Utility under 35 USC 111(a)

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Payment was successfully received in RAM	\$ 180
RAM confirmation Number	5154
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Authorized User	

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		5093IDS.pdf	219499 <small>0a8712709058a0e76cd07d9f57a77ab818215e</small>	yes	4

Multipart Description/PDF files in .zip description

Document Description	Start	End
Transmittal Letter	4	4
Information Disclosure Statement (IDS) Filed (SB/08)	4	4

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	30255 <small>db19138f92a8b18183b7e18051267ac28c33f704</small>	no	2
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Warnings:

Information:

Total Files Size (in bytes): 249754

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